A Parallel Implementation of dual-PEEC for Multicore and Multithreaded CPUs

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Abstract—The Partial Element Equivalent Circuit, as well as other integral techniques, is characterized by full matrices, whose assembly phase is not negligible in terms of computational cost. The aim of the work is to analyze the bottlenecks of the such types of codes and propose an efficient implementation suitable for modern multicore and multithreaded architectures.

Index Terms—Parallel computing, Partial element equivalent circuit, Dual discretization

I. INTRODUCTION

The Partial Element Equivalent Circuit (PEEC), is a well assessed integral technique whose origins go back to the middle '70s in the seminal work by Ruehli [1] and which is still receiving considerable attention [2], [3]. The basic idea is represent an electromagnetic structure by means of an equivalent lumped circuit which can then be solved by SPICElike circuit simulators in the time or frequency domain. The starting point is the equation of the total electric field which, at the observation point \vec{r} and angular frequency ω , takes the form:

$$
\frac{\vec{J}(\vec{r},\omega)}{\sigma} = -j\omega \vec{A}(\vec{r},\omega) - \nabla \varphi(\vec{r},\omega)
$$
 (1)

where \vec{J} is the current density and \vec{A} and φ are the retarded magnetic vector and electric scalar potential, respectively, which can be expressed as:

$$
\vec{A}(\vec{r}, \omega) = \frac{\mu}{4\pi} \int_{\Omega} \frac{\vec{J}(\vec{r}', \omega)}{|\vec{r} - \vec{r}'|} e^{-j\beta |\vec{r} - \vec{r}'|} d\Omega
$$

$$
\varphi(\vec{r}, \omega) = \frac{1}{4\pi\epsilon} \int_{\Omega} \frac{\rho(\vec{r}', \omega)}{|\vec{r} - \vec{r}'|} e^{-j\beta |\vec{r} - \vec{r}'|} d\Omega
$$

where ρ is the volume charge density, β is the wavenumber, \vec{r} the source point and Ω the conductor domain. Standard-PEEC uses pulse basis functions both for current density and for charge density but with different support and the same basis functions are also used as weights in a Galerkin scheme. In the case of non-orthogonal PEEC discretization [4], numerical integration is required for the calculation of partial coefficients [5].

II. DUAL-PEEC FORMULATION

Dual-PEEC is a generalization of PEEC to unstructured triangular tessellations [6]. The scheme makes use of the topological concept of *duality* so that physical variables are associated to spatial elements as proposed by Tonti and shown in Table I. According to such scheme the current density, flowing through the triangles' sides, is interpolated by "face" elements

$$
\vec{J}_m = \frac{1}{t_m} \sum_{k=1}^3 \vec{w}_{km} i_{km}
$$

TABLE I VARIABLES AND THEIR ASSOCIATION TO SPATIAL ENTITIES.

Type	Variable	Spatial Element
source	current, i	dual face, Σ
	charge, q	dual volume, Ω
configuration	voltage, u	primal edge, λ
	potential, φ	primal node P

Fig. 1. Basic geometric entities involved in the discretization and equivalent circuit.

while the charge associated to triangles is represented by pulse functions (see Fig.1(a)). The couple constituted by primal edge/dual face is associated to a circuit branch characterized by:

- resistive coupling (neglected in this study)
- inductive coupling

$$
L_{km,hn} = \frac{\mu_0}{4\pi} \int_{\Sigma_m} \vec{w}_{km} \cdot \int_{\Sigma_n} \frac{\vec{w}_{hn} e^{-j\beta |\vec{r} - \vec{r}'|}}{|\vec{r} - \vec{r}'|} d\Sigma_n d\Sigma_m
$$
\n(2)

• capacitive coupling

$$
p_{m,n} = \frac{1}{4\pi\epsilon_0 S_m S_n} \int_{\Sigma_m} \int_{\Sigma_n} \frac{e^{-j\beta |\vec{r} - \vec{r}'|}}{|\vec{r} - \vec{r}'|} d\Sigma_n d\Sigma_m \quad (3)
$$

The final resulting basic circuit block is shown in Fig. 1(b).

III. PARTIAL ELEMENT CALCULATION

The assembly of the matrices representing the inductive and capacitive coupling requires the computation of the integrals in (2) and (3). When an orthogonal discretization is possible, the computational burden of the partial elements computation is

Fig. 2. CPU time for a 10.000 element mesh.

negligible with respect to solution time, however with nonorthogonal or triangular discretization, the partial element computational cost can become comparable with the solution time [7]. Partial elements with triangular discretization are computed with the following considerations:

- analytical formulas exist for the solution of the inner integral both for inductance and for coefficients of potential [8]
- the outer integral is computed by appropriate quadrature rules on triangles (16 or 25 points)
- since the integration domain is the same for inductances and coefficients of potential, the evaluation of the latter can be obtained at negligible cost as shown in Fig. 2.

The integration routines have been parallelized with the help of OpenMP at the coarsest level, namely all elements are processed in parallel.

IV. RESULTS

Computational results are obtained on a Dell T7500 equipped with dual Intel Xeon X5670 (each processor has 6 cores/12 threads) running at 2.93 GHz with 12Mb cache and a total shared memory of 96Gb. The machine runs Fedora 13 64bit (Kernel 2.6.33 x86 64) and the code was compiled with the Intel Fortran Compiler 11.1 with flags: -03 -m64 -fast -openmp.

Table II and Fig. 3 show the kind of speedup obtainable on the target architecture. The same table demonstrates shows that the code performs in accordance with Amdahl's law $T_{\text{elapsed}}(P) = \left(\frac{f}{P} + 1 - f + O_P P\right) T_{\text{serial}}$ in which P is the number of threads and f is the fraction of parallelized code. It can be seen that the implementation is optimal in the sense that $f = 1$ and the speedup saturation is entirely due to the communication overhead O_P caused by the different processes writing to the shared memory area containing the full matrices.

V. CONCLUSIONS

Dual-PEEC allows the joint calculation of inductance and coefficient of potentials, reducing the overall computational effort with respect to standard nonorthogonal-PEEC. Furthermore, the computation of coefficients has been parallelized

Fig. 3. Speedup for various mesh sizes.

TABLE II COMPUTED AND ESTIMATED PARALLEL PERFORMANCE.

# of threads	Comp. time (s)	Amdahl's law (s)
	measured	$f = 1, O_P = .0013$
serial	263.14	
	267.69	263.48
2	134.97	132.39
4	68.27	67.35
8	35.41	35.86
16	21.73	22.17
24	19.05	19.43
26		19.27
28		19.23
30		19.29
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f: parallel fraction of code, O_P : parallel overhead, P : # of processors

with OpenMP showing good scalability, according to Amdahl's law. In the extended version of the paper it will be shown that additional speedup can be obtained by suitable hierarchic quadrature rules, decreasing the number of quadrature points for weakly interacting elements. Furthermore the recent IBM Power7 architecture will also be evaluated in order to assess if the communication overhead can be reduced.

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